

<b>Notice of References Cited</b>	Application/Control No. 10/508,802	Applicant(s)/Patent Under Reexamination SHIGA, TOMOHISA	
	Examiner Jesse R. Moll	Art Unit 2181	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Computer Organization and Design: The Hardware/Software Interface; John Hennessy and David Patterson, 1997
	V	8051 Tutorial: Addressing Modes; Vault Information Services; Archived on the Internet Archive 15 Feb.2001
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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